

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): T. Sekiguchi, et al.

Filed: March 29, 2004

For: METHOD OF MANUFACTURING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICES HAVING A MEMORY
DEVICE WITH REDUCED BIT LINE STRAY CAPACITY AND
SUCH SEMICONDUCTOR INTEGRATED CIRCUIT DEVICES

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 29, 2004

Sir:

Please amend the above-identified application, prior to examination thereof,
as listed in the following, and as set forth on the following pages:

Amendments to the Specification; and

Remarks are included following the amendments.